

AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph beginning at page 13, line 4 with the following:

The writing circuit 9 is configured by a constant current source transistor (current absorption control transistor) 12, a latch switch 13, a NAND-circuit 14, and a writing latch 15. The constant current source transistor 12 and the latch switch 13 are, for example, N-channel MOS (Metal Oxide Semiconductor) transistors.

IN THE ABSTRACT:

Please replace the original abstract with the abstract on the attached separate sheet.